Coss Cost	
	U.S. UTILITY PATENT APPLICATION

SCANNED TO 4 QA. Am

APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER	
09/802443	D	257 428	61.2	2811770	Yeusikou .	€t.
Rajendra				2825		
🏌 Marcos Ka 🤾 Walter Bu						

Flip chip-in-leadframe package and process

E

PTO-2040 12/99

RATENT NUMBER

## PREPARED AND APPROVED FOR ISSUE

↑ ISSUING CLASSIFICATION								
ORI	ANNE	<i>i</i> \		CROSS R	CROSS REFERENCE(S)			
CLASS	SUBCLASS	/ CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)			CK)		
438	612	,438,	6/3	616	\			
INTERNATIONAL	DNAL CLASSIFICATION							
1011	21/44/	1						
	7/ /							
$\Box$	7							
	<u> </u>			Continued on Issue Slip Inside File Jacket				æt

	. 1				λ	
TERMINAL	QRAWINGS /			CLAIMS ALLOWED		
L DISCLAIMER	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims $\mathcal{R}\mathcal{O}$	Pfint Claim for O.G.	
a) The term of this patent	+		7	NOTICE OF AK	LOWANCE MAILED	
subsequent to (date) has been disclaimed.	(Assistant	Examiner)	(Date)			
b) The term of this patent shall						
not extend beyond the expiration date of U.S Patent. No				ISSUE FEE		
				Amount Due	Date Paid	
	(Primary Examiner) (Date)					
c) The terminalmonths of				ISSUE BA	TCH NUMBER	
this patent have been disclaimed.	(Legal tristrum	ente Examiner)	(Date)			
WARNING:						

The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 388. Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.

Form PTO-438A (Rev. 10/97)

(LABEL AREA)